

**ST.ANN'S COLLEGE OF ENGINEERING AND TECHNOLOGY:: CHIRALA**  
**DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING**

**LECTURE SCHEDULE**

**SUBJECT: COMPUTER ORGNIZATION**

**ACADEMIC YEAR: 2017-18**

**FACULTY:Y.CHITTI BABU**

**YEAR-SEM & SEC: II CSE– II ( A )**

**No. of Lectures per Week: 4+1\*(Tutorial)**

S.no	Date	UNIT	TOPIC TO BE COVERED
1	20/11/2017	I	Introduction
2	21/11/2017		Functional unit
3	23/11/2017		Basic Operational concepts
4	24/11/2017		Bus structures
5	25/11/2017		System Software
6	27/11/2017		Performance
7	28/11/2017		The history of computer development.
8	30/11/2017		Revision(ppt)
9	<b>02/12/2017</b>		<b>Tutorial</b>
10	04/12/2017	<b>SlipTest-I</b>	
11	05/12/2017	II	Register Transfer Notation
12	07/12/2017		Assembly Language Notation
13	08/12/2017		Basic Instruction Types
14	11/12/2017		Basic Instruction Types
15	12/12/2017		Addressing Modes
16	14/12/2017		Addressing Modes
17	15/12/2017		Basic Input/output Operations
18	<b>16/12/2017</b>		<b>Tutorial</b>
19	18/12/2017		The role of Stacks and Queues in computer programming equation
20	19/12/2017		Logic Instructions
21	21/12/2017		shift and Rotate Instructions
22	22/12/2017		Revision(ppt)
23	<b>23/12/2017</b>		<b>Slip Test-2</b>
24	26/12/2017	III	Introduction to instructions
25	28/12/2017		Arithmetic and Logic Instructions
26	29/12/2017		Arithmetic and Logic Instructions
27	<b>30/12/2017</b>		<b>Tutorial</b>
28	01/01/2018		Branch Instructions
29	02/01/2018		Branch Instructions
30	04/01/2018		Addressing Modes
31	05/01/2018		Addressing Modes
32	<b>06/01/2018</b>		Input/output Operations
33	08/01/2018		<b>Tutorial</b>
34	09/01/2018		Input/output Operations
35	11/01/2018		Revision(ppt)
36	12/01/2018		Revision
37	18/01/2018	IV	Revision
38	19/01/2018		Revision
39	20/01/2018		Revision
40	22/01/2018		Revision
41	23/01/2018		Revision
42	25/01/2018		Accessing I/O Devices
43	<b>27/01/2018</b>		<b>Tutorial</b>
44	29/01/2018		Direct Memory Access
45	30/01/2018		Synchronous Bus, Asynchronous Bus

46	01/02/2018		Interrupts
47	02/02/2018		Interface Circuits
48	<b>03/02/2018</b>		<b>Tutorial</b>
49	05/02/2018		Peripheral Component Interconnect (PCI) Bus
50	06/02/2018		Universal Serial Bus (USB)
51	08/02/2018		Universal Serial Bus (USB)
52	09/02/2018		Revision(ppt)
53	<b>10/02/2018</b>		SlipTest-3
54	15/02/2018	V	Basic memory circuits
55	16/02/2018		Memory System Consideration
56	<b>17/02/2018</b>		<b>Tutorial</b>
57	19/02/2018		Read-Only Memory
58	20/02/2018		Cache Memorie
59	22/02/2018		interleaving
60	23/02/2018		Magnetic Hard Disks, Optical Disks
61	24/02/2018		Revision(ppt)
62	26/02/2018		SlipTest-4
63	27/02/2018		VI
64	01/03/2018	Performing An Arithmetic OrLogic Operation	
65	<b>03/03/2018</b>	<b>Tutorial</b>	
66	05/03/2018	Fetching A Word From Memory, Execution of Complete Instruction	
67	06/03/2018	Hardwired Control, Microinstructions, Micro program Sequencing, Wide Branch Addressing	
68	08/03/2018	Microinstructions with next –Address Field	
69	09/03/2018	Revision	
70	12/03/2018	Revision	
71	13/03/2018	Revision	
72	15/03/2018	Revision	
73	16/03/2018	Revision	
74	<b>17/03/2018</b>	Revision	
75	19/03/2018	Revision	
76	20/03/2018	Revision	
77	22/03/2018	Revision	
78	23/03/2018	Revision	
79	24/03/2018	Revision	

**TEXT BOOKS:**

1. Computer Organization, Carl Hamacher, Zvonks Vranesic, Safea Zaky, 5th Edition, McGraw Hill.
2. Computer Architecture and Organization, John P. Hayes, 3rd Edition, McGraw Hill.

**REFERENCE BOOKS:**

1. Computer Organization and Architecture – William Stallings Sixth Edition, Pearson/PHI
2. Structured Computer Organization – Andrew S. Tanenbaum, 4th Edition PHI/Pearson
3. Fundamentals or Computer Organization and Design, - Sivaraama Dandamudi Springer Int.Edition.
4. “Computer Organization and Design: The Hardware/Software Interface” by David A. Patterson and John L. Hennessy.
5. J .P. Hayes, "Computer Architecture and Organization", McGraw-Hill, 1998.

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**ACADEMIC YEAR: 2017-18**

**FACULTY:Y.CHITTI BABU**

**YEAR-SEM & SEC: II CSE- II ( B )**

**No. of Lectures per Week: 4+1\*(Tutorial)**

S.no	Date	UNIT	TOPIC TO BE COVERED
1	20/11/2017	I	Introduction
2	21/11/2017		Functional unit
3	23/11/2017		Basic Operational concepts
4	24/11/2017		Bus structures
5	25/11/2017		System Software
6	27/11/2017		Performance
7	28/11/2017		The history of computer development.
8	30/11/2017		The history of computer development.
9	02/12/2017		Revision(ppt)
10	<b>04/12/2017</b>		<b>SlipTest-I</b>
11	05/12/2017	II	Register Transfer Notation
12	07/12/2017		Assembly Language Notation
13	08/12/2017		Basic Instruction Types
14	<b>11/12/2017</b>		<b>Tutorial</b>
15	12/12/2017		Addressing Modes
16	14/12/2017		Addressing Modes
17	15/12/2017		Basic Input/output Operations
18	16/12/2017		The role of Stacks and Queues in computer programming equation
19	<b>18/12/2017</b>		<b>Tutorial</b>
20	19/12/2017		Logic Instructions
21	21/12/2017		shift and Rotate Instructions
22	22/12/2017		Revision(ppt)
23	23/12/2017		<b>Slip Test-2</b>
24	26/12/2017	III	Introduction to instructions
25	28/12/2017		Arithmetic and Logic Instructions
26	29/12/2017		Arithmetic and Logic Instructions
27	30/12/2017		Branch Instructions
28	<b>01/01/2018</b>		<b>Tutorial</b>
29	02/01/2018		Branch Instructions
30	04/01/2018		Addressing Modes
31	05/01/2018		Addressing Modes
32	06/01/2018		Input/output Operations
33	<b>08/01/2018</b>		<b>Tutorial</b>
34	09/01/2018	Input/output Operations	
35	11/01/2018	Revision(ppt)	
36	12/01/2018	Revision	
37	18/01/2018	IV	Revision
38	19/01/2018		Revision
39	20/01/2018		Revision
40	22/01/2018		Revision
41	23/01/2018		Revision
42	25/01/2018		Accessing I/O Devices
43	27/01/2018		Direct Memory Access

44	<b>29/01/2018</b>		<b>Tutorial</b>
45	30/01/2018		Synchronous Bus, Asynchronous Bus
46	01/02/2018		Interrupts
47	02/02/2018		Interface Circuits
48	03/02/2018		Peripheral Component Interconnect (PCI) Bus
49	<b>05/02/2018</b>		<b>Tutorial</b>
50	06/02/2018		Universal Serial Bus (USB)
51	08/02/2018		Universal Serial Bus (USB)
52	09/02/2018		Revision(ppt)
53	10/02/2018		<b>SlipTest-3</b>
54	15/02/2018	V	Basic memory circuits
55	16/02/2018		Memory System Consideration
56	17/02/2018		Read-Only Memory
57	<b>19/02/2018</b>		<b>Tutorial</b>
58	20/02/2018		Cache Memory
59	22/02/2018		interleaving
60	23/02/2018		Magnetic Hard Disks, Optical Disks
61	24/02/2018		Revision(ppt)
62	<b>26/02/2018</b>		<b>SlipTest-4</b>
63	27/02/2018		
64	01/03/2018		Performing An Arithmetic OrLogic Operation
65	03/03/2018		Fetching A Word From Memory, Execution of Complete Instruction
66	<b>05/03/2018</b>		<b>Tutorial</b>
67	06/03/2018		Hardwired Control, Microinstructions, Micro program Sequencing, Wide Branch Addressing
68	08/03/2018		Microinstructions with next –Address Field
69	09/03/2018		Revision
70	<b>12/03/2018</b>	VI	Revision
71	13/03/2018		Revision
72	15/03/2018		Revision
73	16/03/2018		Revision
74	17/03/2018		Revision
75	19/03/2018		Revision
76	20/03/2018		Revision
77	22/03/2018		Revision
78	23/03/2018		Revision
79	24/03/2018		Revision

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**YEAR-SEM & SEC: II CSE– II ( C)**

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S.no	Date	UNIT	TOPIC TO BE COVERED	
1	20/11/2017	I	Introduction	
2	21/11/2017		Functional unit	
3	22/11/2017		Basic Operational concepts	
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5	25/11/2017		System Software	
6	27/11/2017		Performance	
7	28/11/2017		The history of computer development.	
8	29/11/2017		The history of computer development.	
9	30/11/2017		Revision(ppt)	
10	<b>02/12/2017</b>			<b>SlipTest-I</b>
11	04/12/2017	II	Register Transfer Notation	
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23	21/12/2017		Revision(ppt)	
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25	26/12/2017	III	Introduction to instructions	
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36	10/01/2018	Revision(ppt)		
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40	20/01/2018		Revision	
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43	24/01/2018		Accessing I/O Devices	
44	26/01/2018		Direct Memory Access	
45	<b>27/01/2018</b>			<b>Tutorial</b>

46	29/01/2018		Synchronous Bus, Asynchronous Bus
47	30/01/2018		Interrupts
48	31/01/2018		Interface Circuits
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75	14/03/2018	Revision	
76	15/03/2018	Revision	
77	17/03/2018	Revision	
78	19/03/2018	Revision	
79	20/03/2018	Revision	
80	21/03/2018	Revision	
81	22/03/2018	Revision	
82	24/03/2018	Revision	

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