

ST. ANN'S COLLEGE OF ENGINEERING & TECHNOLOGY: CHIRALA
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

LECTURE SCHEDULE

SUBJECT: Computer Organization

ACADEMIC YEAR: 2019 -20

NAME: Dr. A.Tirupathiah

YEAR & SEM/SECTION: II-II (B)

No. of Lectures per week: 5+1* (Tutorial)

S.No	DATE	UNIT	TOPICS
1	18-11-2019	I	Basic Structure of Computers
2	19-11-2019		Functional units
3	20-11-2019		Basic Operational concepts
4	21-11-2019		Basic Operational concepts
5	22-11-2019		Bus structures
6	23-11-2019		System Software
7	25-11-2019		Performance
8	26-11-2019		TUTORIAL
9	27-11-2019		Performance
10	28-11-2019		The history of computer development
11	29-11-2019		The history of computer development
12	30-11-2019		UNIT TEST-I
13	02-12-2019	II	Machine Instruction and Programs
14	03-12-2019		TUTORIAL
15	04-12-2019		Instruction and Instruction Sequencing
16	05-12-2019		Register Transfer Notation
17	06-12-2019		Assembly Language Notation
18	07-12-2019		Basic Instruction Types
19	09-12-2019		Addressing Modes
20	10-12-2019		TUTORIAL
21	11-12-2019		Addressing Modes
22	12-12-2019		Basic Input/output Operations
23	13-12-2019		The role of Stacks and Queues in computer programming
24	14-12-2019		Component of Instructions
25	16-12-2019		Logic Instructions
26	17-12-2019		TUTORIAL

27	18-12-2019	II	Shift and Rotate Instructions
28	19-12-2019		UNIT TEST-II
29	20-12-2019	III	Types of Instructions
30	21-12-2019		Arithmetic Instructions
31	23-12-2019		Logic Instructions
32	26-12-2019		Branch Instructions
33	27-12-2019		Branch Instructions
34	28-12-2019		Addressing Modes
35	30-12-2019		Addressing Modes
36	31-12-2019		TUTORIAL
37	01-01-2020		Addressing Modes
38	02-01-2020		Input/output Operations
39	03-01-2020		Input/output Operations
40	04-01-2020		Revision
41	06-01-2020		Revision
42	07-01-2020		Revision
43	08-01-2020		Revision
44	09-01-2020		Revision
45	10-01-2020		Revision
46	13-01-2020		Revision
47	18-01-2020		Revision
48	20-01-2020		Revision
49	21-01-2020	Revision	
50	22-01-2020	Revision	
51	23-01-2020	Revision	
52	24-01-2020	IV	Input/Output Organization
53	25-01-2020		Accessing I/O Devices
54	27-01-2020		Interrupts: Interrupt Hardware
55	28-01-2020		TUTORIAL
56	29-01-2020		Enabling and Disabling Interrupts

57	30-01-2020	IV	Handling Multiple Devices
58	31-01-2020		Direct Memory Access(DMA)
59	01-02-2020		Buses: Synchronous Bus, Asynchronous Bus
60	03-02-2020		Interface Circuits, Standard I/O Interface
61	04-02-2020		TUTORIAL
62	05-02-2020		Peripheral Component Interconnect (PCI) Bus
63	06-02-2020		Universal Serial Bus (USB)
64	07-02-2020		UNIT TEST-IV
65	10-02-2020	V	The MEMORY SYSTEMS: Basic Memory Circuits
66	11-02-2020		TUTORIAL
67	12-02-2020		Memory System Considerations
68	13-02-2020		Read-Only Memories: ROM, PROM
69	14-02-2020		EPROM, EEPROM, Flash Memory
70	15-02-2020		Cache Memories: Mapping Functions
71	17-02-2020		Cache Memories: Mapping Functions
72	18-02-2020		TUTORIAL
73	19-02-2020		Interleaving
74	20-02-2020		Secondary Storage: Magnetic Hard Disks
75	21-02-2020		Magnetic Hard Disks
76	22-02-2020		Optical Disks
77	25-02-2020		TUTORIAL
78	26-02-2020		UNIT TEST-V
79	27-02-2020	VI	Processing Unit: Fundamental Concepts
80	28-02-2020		Register Transfers
81	29-02-2020		Performing an Arithmetic Or Logic Operation
82	02-03-2020		Fetching a Word from Memory
83	03-03-2020		TUTORIAL
84	04-03-2020		Execution of Complete Instruction
85	05-03-2020		Hardwired Control
86	06-03-2020		Micro programmed Control
87	07-03-2020		Microinstructions, Micro program Sequencing

88	10-03-2020	VI	TUTORIAL
89	11-03-2020		Wide Branch Addressing
90	12-03-2020		Microinstructions with next Address field
91	13-03-2020		Revision
92	16-03-2020		Revision
93	17-03-2020		Revision
94	18-03-2020		Revision
95	19-03-2020		Revision
96	20-03-2020		Revision
97	21-03-2020		Revision
98	23-03-2020		Revision
99	24-03-2020		Revision
100	26-03-2020		Revision
101	27-03-2020		Revision
102	28-03-2020		Revision
103	30-03-2020		Revision

TEXT BOOKS:

1. Computer Organization, Carl Hamacher, ZvonksVranesic, SafeaZaky, 5th Edition, McGraw Hill.
2. Computer Architecture and Organization , John P. Hayes ,3rd Edition, McGraw Hill.

REFERENCE BOOKS:

1. Computer Organization and Architecture – William Stallings Sixth Edition, Pearson/PHI
2. Structured Computer Organization – Andrew S. Tanenbaum, 4th Edition PHI/Pearson
3. Fundamentals or Computer Organization and Design, - SivaraamaDandamudi Springer Int. Edition.
4. “Computer Organization and Design: The Hardware/Software Interface” by David A. Patterson and John L. Hennessy.
5. J .P. Hayes, "Computer Architecture and Organization", McGraw-Hill, 1998.

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